VLSI Chip Design With The Hardware Description Language VERILOG: An Introduction Based On A Large RISC Processor Design

Synopsis

The art of transforming a circuit idea into a chip has changed permanently. Formerly, the electrical, physical and geometrical tasks were predominant. Later, mainly net lists of gates had to be constructed. Nowadays, hardware description languages (HDL) similar to programming languages are central to digital circuit design. HDL-based design is the main subject of this book.After emphasizing the economic importance of chip design as a key technology, the book deals with VLSI design (Very Large Scale Integration), the design of modern RISC processors, the hardware description language VERILOG, and typical modeling techniques. Numerous examples as well as a VERILOG training simulator are included on a disk.

Book Information

Hardcover: 360 pages

Publisher: Springer; Har/Dskt edition (February 22, 1996)

Language: English

ISBN-10: 3540600329

ISBN-13: 978-3540600329

Product Dimensions: 9.5 x 6.4 x 1.2 inches

Shipping Weight: 1.6 pounds

Average Customer Review: Be the first to review this item

Best Sellers Rank: #2,435,370 in Books (See Top 100 in Books)   #97 in Books > Engineering & Transportation > Engineering > Electrical & Electronics > Circuits > VLSI & ULSI   #274 in Books > Computers & Technology > Hardware & DIY > Microprocessors & System Design > Microprocessor Design   #389 in Books > Computers & Technology > Programming > Software Design, Testing & Engineering > Logic

VLSI Chip Design with the Hardware Description Language VERILOG: An Introduction Based on a Large RISC Processor Design The Verilog PLI Handbook: A User's Guide and Comprehensive Reference on the Verilog Programming Language Interface Digital Design (Verilog): An Embedded Systems Approach Using Verilog Embedded SoPC Design with Nios II Processor and Verilog Examples Introduction to RISC Assembly Language Programming Digital VLSI Design with Verilog: A Textbook from Silicon Valley Polytechnic Institute Digital VLSI Design with Verilog: A Textbook from Silicon Valley Technical Institute Body Language: Body Language Training - Attract Women & Command Respect, by Mastering Your High Status Body Language (Body Language Attraction,

Body Language ... Language Secrets, Nonverbal Communication) Digital VLSI Chip Design with Cadence and Synopsys CAD Tools Chip Design for Submicron VLSI: CMOS Layout and Simulation Digital Systems Design and Prototyping: Using Field Programmable Logic and Hardware Description Languages Description of the Colt's double-action revolver, caliber .38, with rules for management, memoranda of trajectory, and description of ammunition Sing You Home Large Print (Large Print, companion soundtrack, Large Print) Word Search Puzzles Large Print: Large print word search, Word search books, Word search books for adults, Adult word search books, Word search puzzle books, Extra large print word search Circuits, Interconnections, and Packaging for Vlsi (Addison-Wesley VLSI systems series) ECHO USER GUIDE: The Official User Guide For Using Your Echo (technology mobile communication kindle alexa computer hardware) (Echo ... & Technology Ebooks Hardware & DYI) MIPS RISC Architecture (2nd Edition) Digital Design: With an Introduction to the Verilog HDL 5th Ed. By Morris Mano (International Economy Edition) Digital Design with RTL Design, VHDL, and Verilog BODY LANGUAGE : Decoding Alpha Male Body Language, Instantly Attract Any Woman Without Saying a Single word. (Body Language 101, Alpha male, Attract woman, ... Seduce Women, Eye Contact, Body Language)